

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
Ravi Iyer

Parent Application Serial No. 08/599,675
Parent Application Filed: February 12, 1996

Serial No.: Unknown

Filed: Herewith

For: PLANARIZATION USING PLASMA
OXIDIZED AMORPHOUS SILICON

Assistant Commissioner
for Patents
Washington, D.C. 20231

Sir:

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §§ 1.97(b)(1) AND 1.98**

In compliance with the duty of disclosure under 37 C.F.R. § 1.56(a), Applicants respectfully request that this Information Disclosure Statement be entered and that the listed references be considered by the Examiner and made of record. Copies of the listed references are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made, as an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b), or as a representation that no other possibly material information, as defined in 37 C.F.R. § 1.56(b), exists.

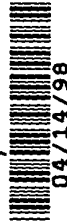
The following information is listed below in accordance with 37 C.F.R. § 1.98. Any explanation of non-English language documents contained in this Information Disclosure Statement is believed to constitute a concise explanation of the relevance of the listed reference as it is presently understood by the individual designated in § 1.56(c) most knowledgeable about the content of the listed reference, in accordance with 37 C.F.R. § 1.98(a)(3).

Group Art Unit: 1104

Examiner: Whipple, M.

Atty. Dkt: MICS:0015--2/FLE
93-118.02

JCS25 U.S. PTO
09/059865



Proctor
Handwritten signature
5/20/98

U.S. PATENTS

Patentee	Patent Number	Issue Date
Asit K. Ray et al.	4,323,589	04/06/82
Tadashi Kiriseko	4,408,387	10/11/83
Henry J. Geipel, Jr. et al.	4,527,325	07/09/85
Grzegorz Kaganowicz et al.	4,576,829	03/18/86
Lorenzo Faraone et al.	4,604,304	08/05/86
Peter J. Zdebel	5,026,665	06/25/91
Ralph J. Jaccodine et al.	5,043,224	08/27/91
Junichi Sato	5,182,221	01/26/93
Daitei Shin et al.	5,310,720	05/10/94
David M. Dobuzinsky et al.	5,330,935	07/19/94
Peter S. Ying	5,384,288	01/24/95
Hong S. Yang et al.	5,387,539	02/07/95
Toshimasa Matsuoka et al.	5,391,508	02/21/95
Thomas A. Figura et al.	5,438,016	08/01/95
Jae K. Kim	5,466,637	11/14/95
Thomas A. Figura et al.	5,472,904	12/05/95
Hongyong Zhang et al.	5,508,209	04/16/96
Yung-Hao Lin et al.	5,567,638	10/22/96

FOREIGN PATENTS

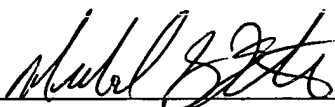
Country or Issuing Patent Office	Document Number	Publication Date
Japan	2-114641	04/26/90
Japan	60-70740	04/22/85

PUBLICATIONS

Author	Title	Relevant Pages	Date	Place of Publication
Badih El-Kareh	Fundamentals of Semiconductor Processing Technology	128-129		Kluwer Academic Publishers-Boston

Respectfully submitted,

Date: April 14, 1998



Michael G. Fletcher
Reg. No. 32,777
Fletcher, Yoder & Edwards
P.O. Box 692289
Houston, TX 77269-2289
(281) 970-4545